

Designing with NXP i.MX 8M Mini

Presented by: Hands-On Training, an NXP Proven Partner

Designing with NXP provides an introduction to the i.MX 8M Mini applications processor which combines high-performance computing with multimedia, graphics, and audio capabilities.

This full-day training offers a combination of lecture and hands-on labs using the Variscite DART-MX8-MINI. Attendees will run applications, build a Yocto project from the source code, and debug the system with Eclipse.

Lunch will be provided.



Topics Covered

- i.MX 8M Mini architecture
- Heterogeneous multicore processing with Arm Cortex-A53 and Cortex-M4
- How to choose the right i.MX 8M Mini device for your project
- Multimedia, graphics and audio capabilities of the i.MX 8M Mini
- Clock, reset, power management and debug strategy
- Memory architecture
- Boot processes, including Secure Boot
- DART-MX8-MINI SOM hardware
- Running applications on Yocto
- Building a Linux Kernel from source code

**Seats are limited.
Register now!**

Dayton	11/19	9:00-4:30	Indiana Wesleyan University Dayton Education & Conference Center	2912 Springboro West Road, Dayton, OH 45439
Chicago	11/20	9:00-4:30	Arrow Electronics	1162 Springlake Dr, Itasca, IL 60143
Minneapolis	11/21	9:00-4:30	Arrow Electronics	10900 Hampshire Ave South, Suite 180, Bloomington, MN 55438
Toronto	11/26	9:00-4:30	Arrow Electronics	171 Superior Blvd, Unit #2, Mississauga, ON L5T 2L6
Montreal	11/27	9:00-4:30	Hampton Inn & Suites	1900 Route Transcanadienne, Dorval QC H9P 2N4
Vancouver	11/28	9:00-4:30	TBD	
New Jersey	12/2	9:00-4:30	TBD	
Boston	12/3	9:00-4:30	Arrow Electronics	4 Technology Drive, Peabody, MA 01960
Raleigh	12/4	9:00-4:30	Hilton Garden Inn	4620 South Miami Blvd, Durham, NC 27703
Atlanta	12/5	10:00-5:30	TBD	